

Figure 1

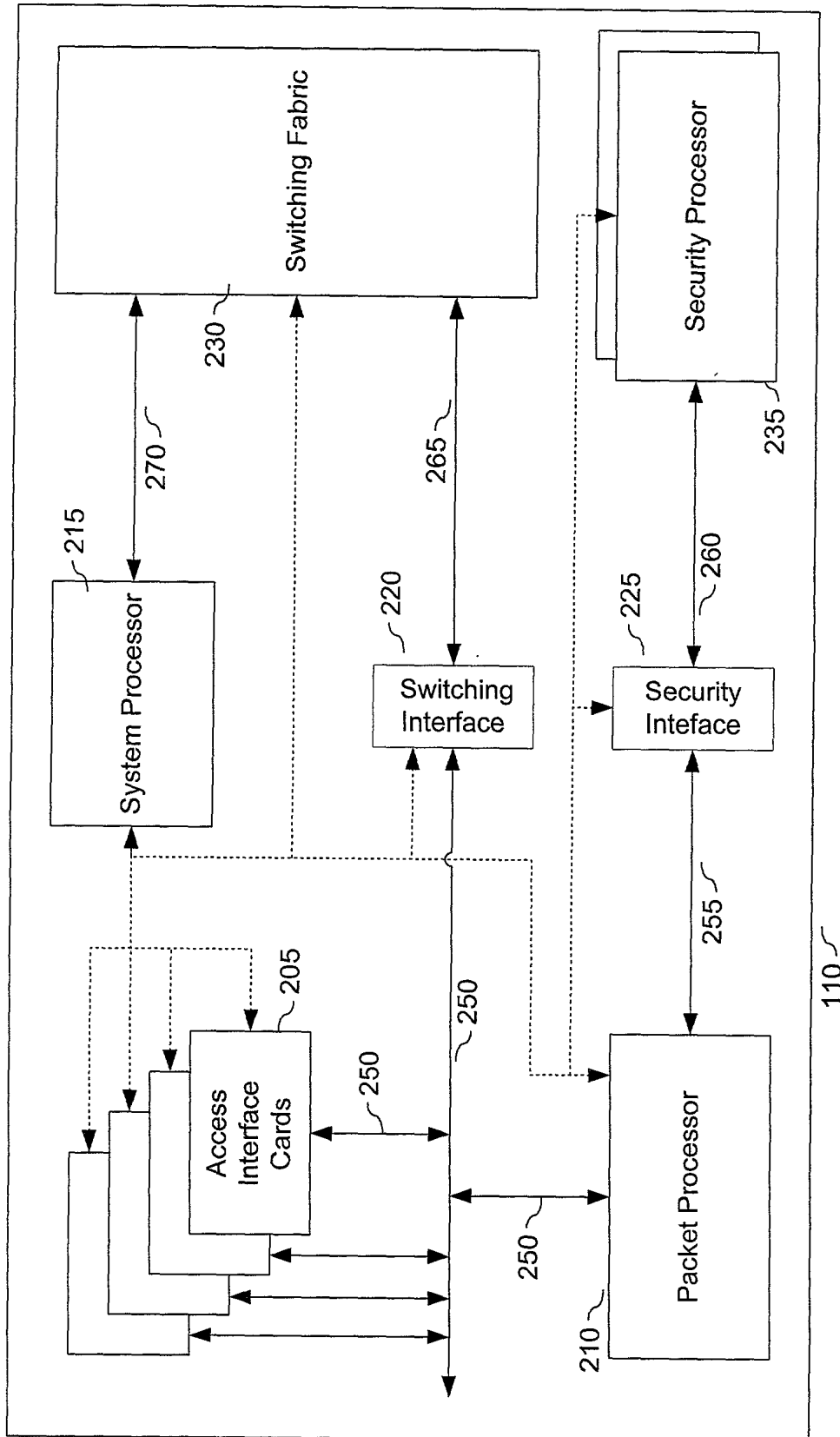
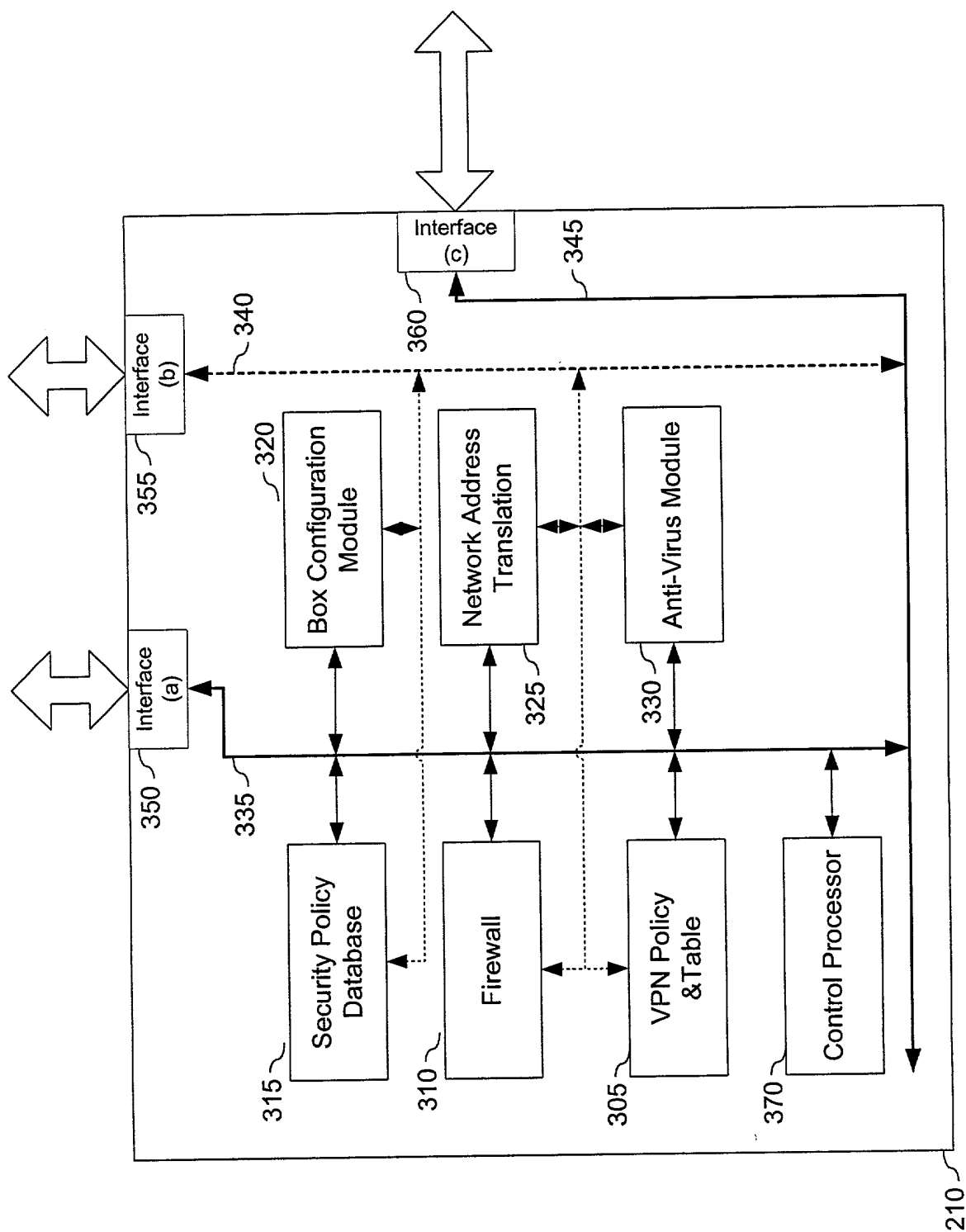


Figure 2



Packet Processor

Figure 3

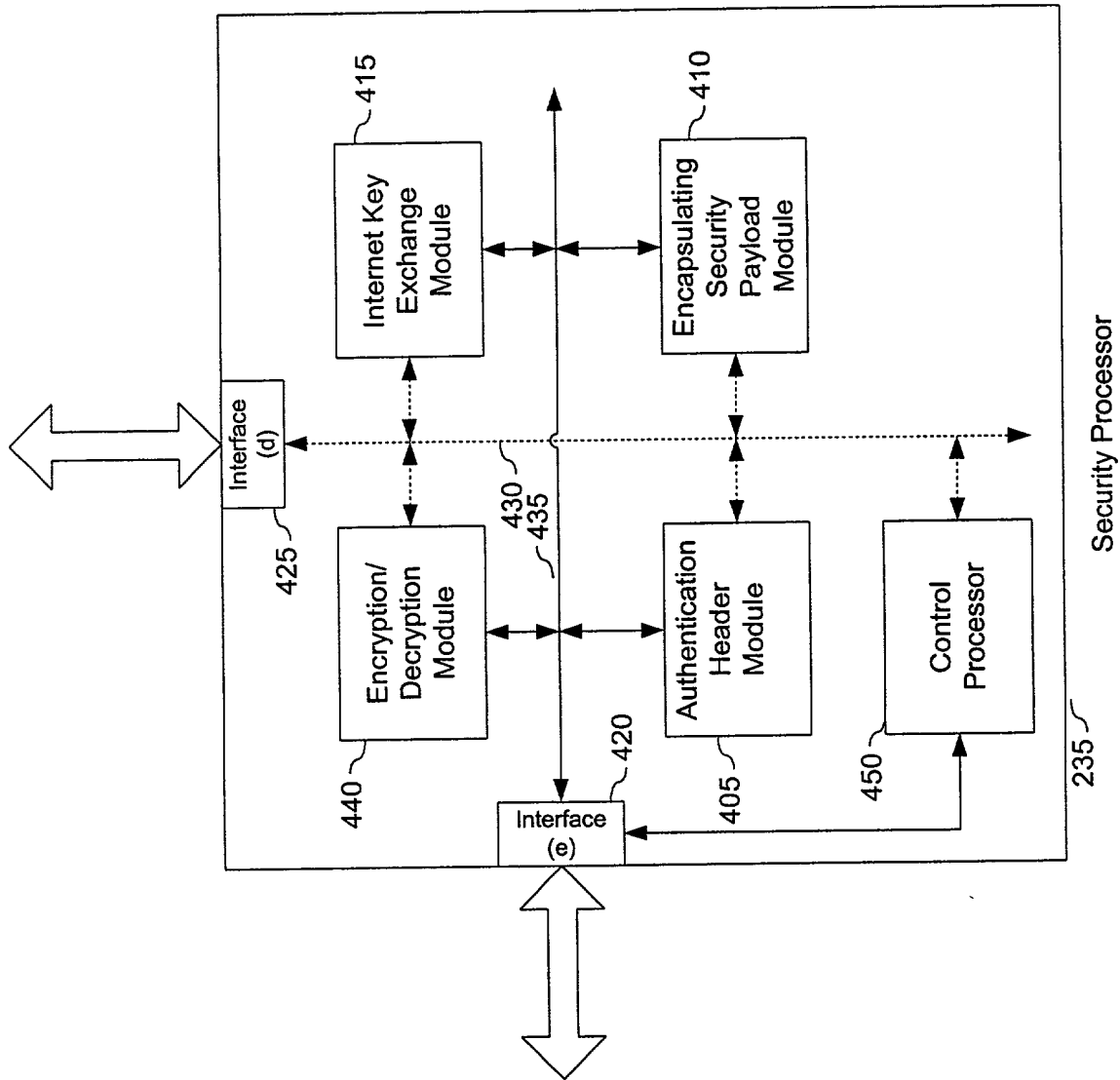


Figure 4

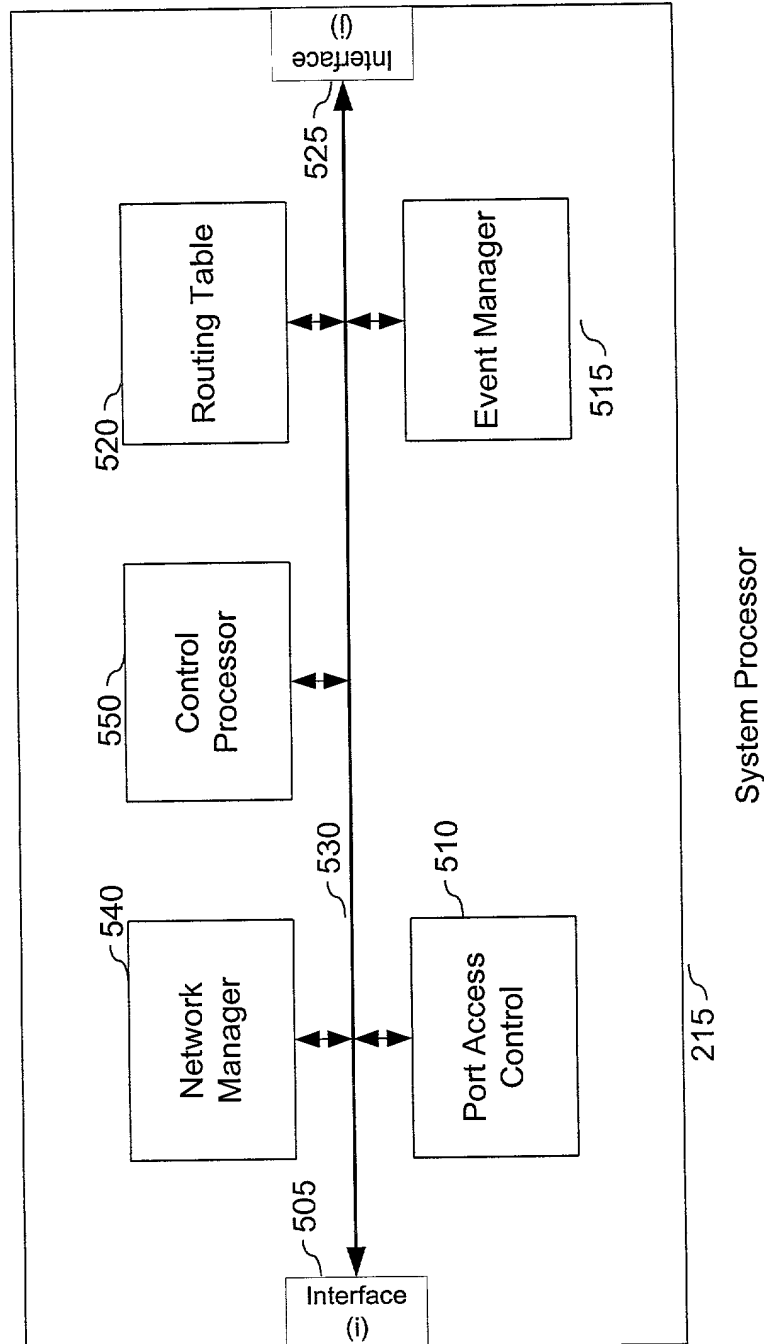


Figure 5

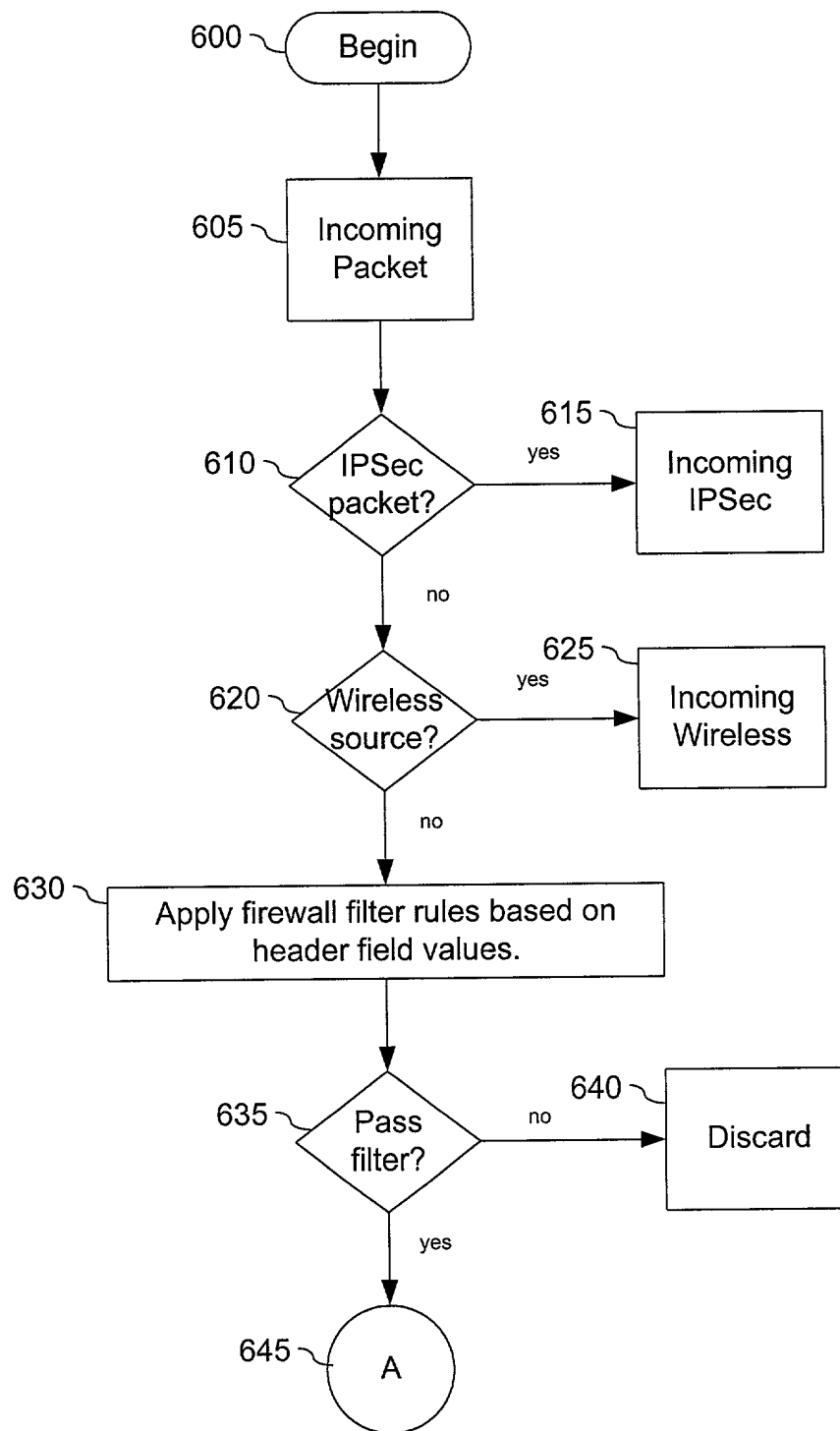


Figure 6A

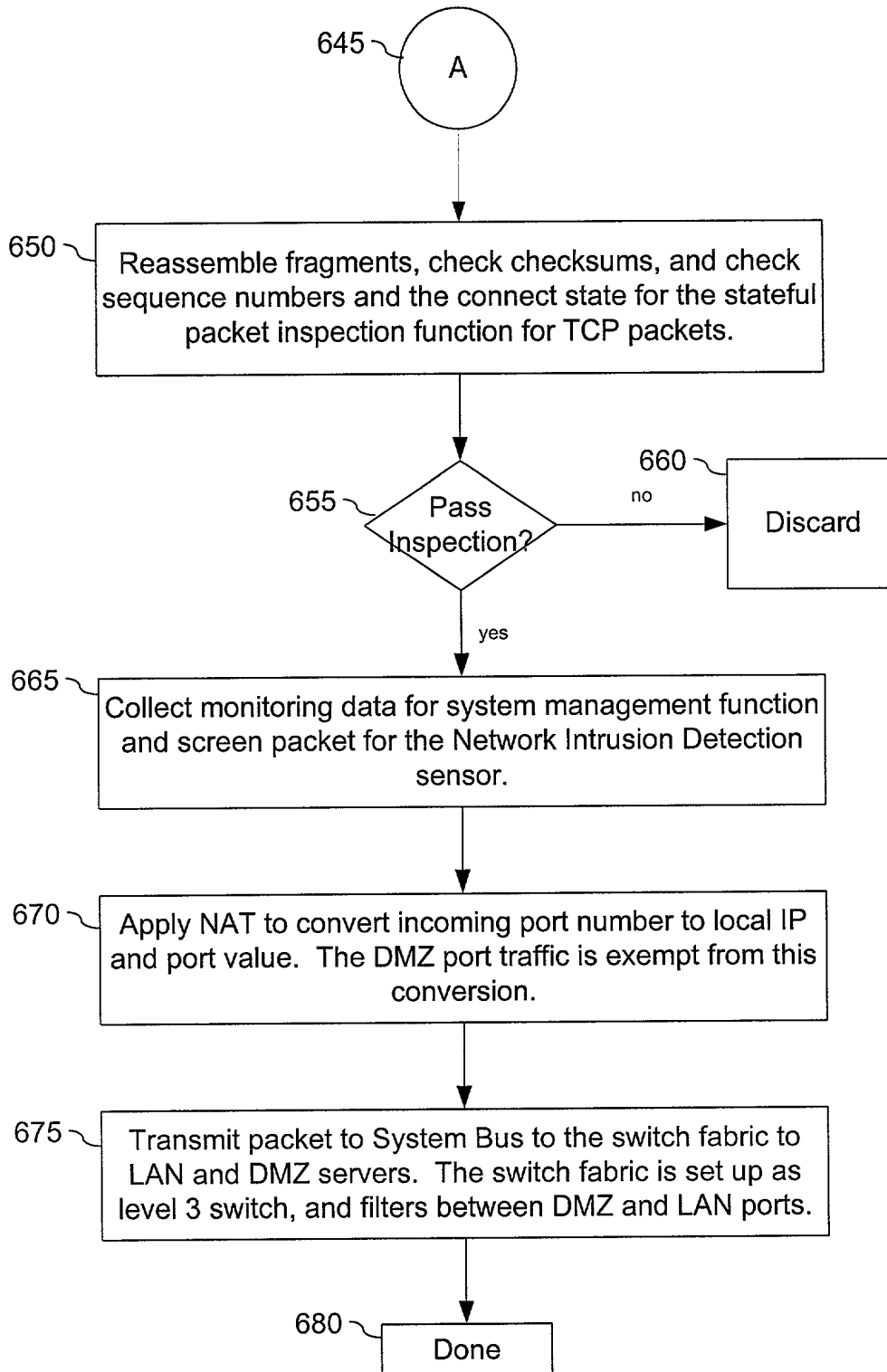


Figure 6B

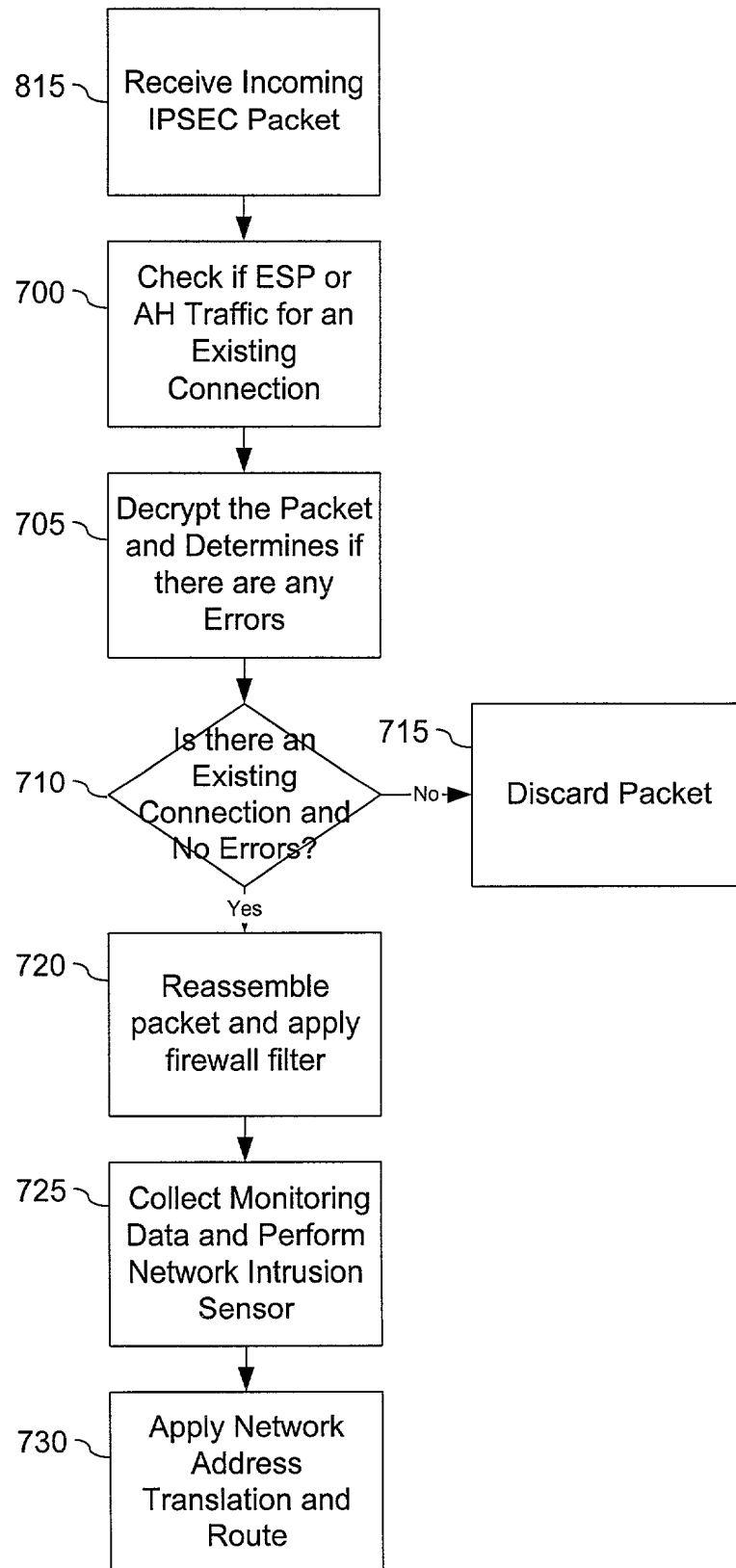


Figure 7



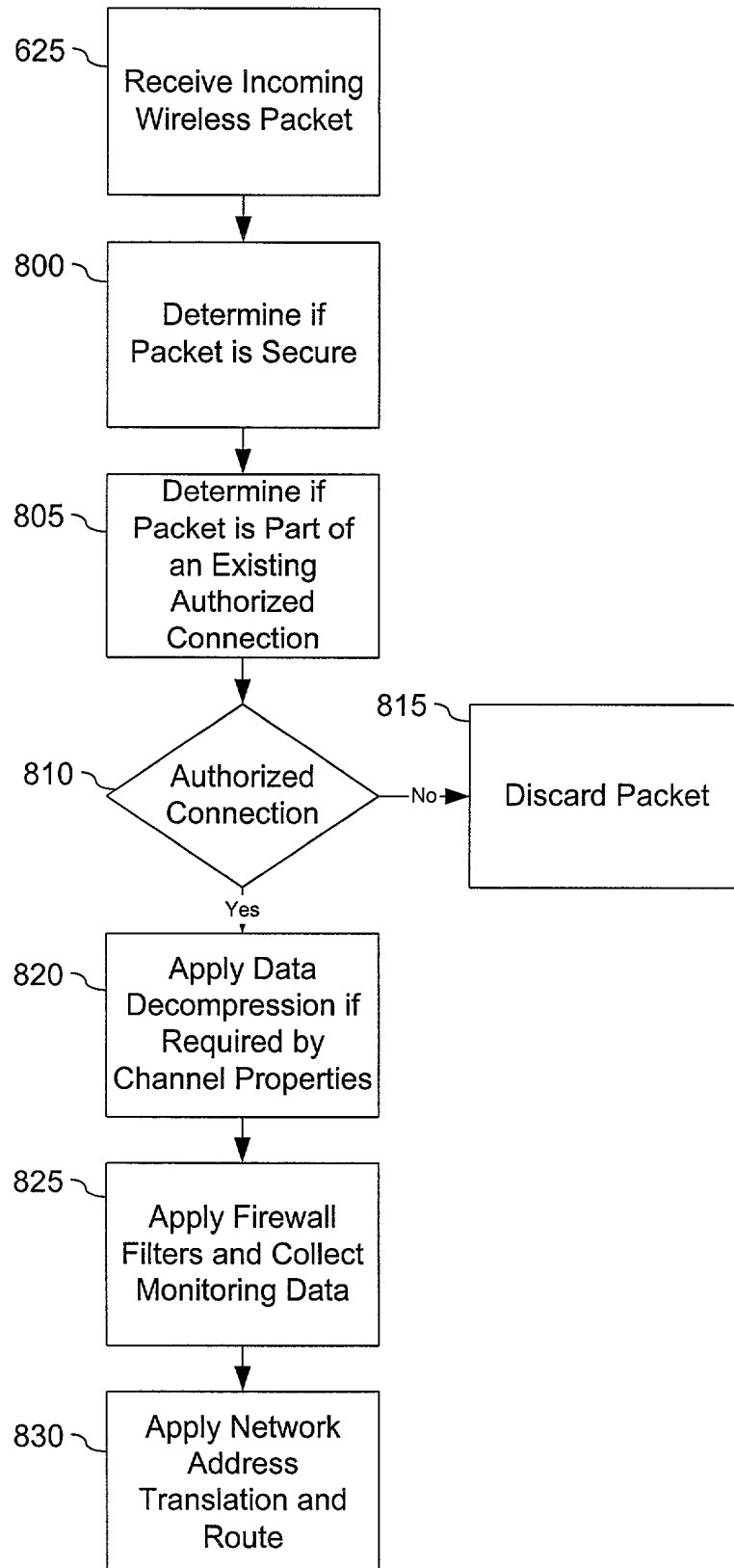


Figure 8

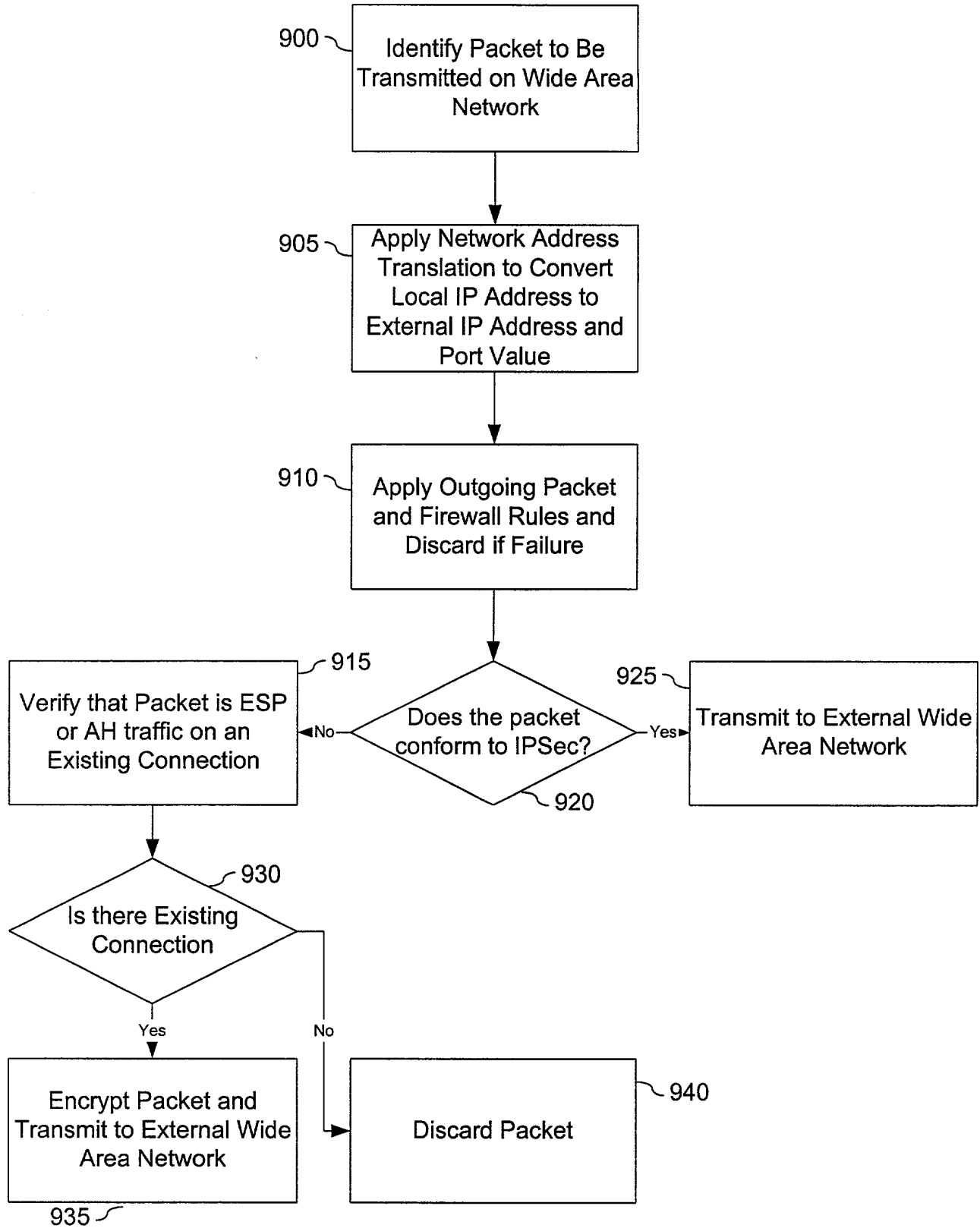


Figure 9

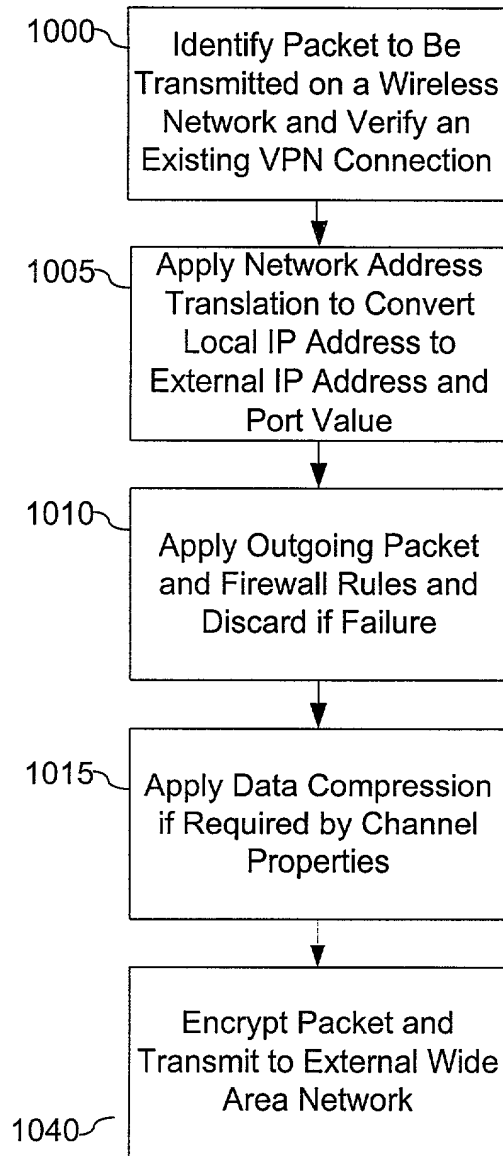


Figure 10